ABSTRACT:

AGC circuit comprising a digitally controlled amplifier being provided with a gain control loop including a level detector, a threshold circuit and a digital gain control signal generator being coupled to a gain control input of the digitally controlled amplifier for supplying thereto a digital gain control signal. To prevent astable gain settings of the digitally controlled amplifier from occurring a continuously controlled amplifier is being coupled between an output of the digitally controlled amplifier and the level detector, an output of the level detector being coupled to a gain control input of the continuously controlled amplifier for supplying thereto an continuous gain control signal, the gain variation range of the continuously controlled amplifier at least corresponding to the gain step variation of the digitally controlled amplifier at an incremental step of said digital gain control signal.

Figure 1

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